

METHOD AND DEVICE FOR SEQUENTIAL READOUT OF A MEMORY
WITH ADDRESS JUMP

Abstract of the Disclosure

A memory implementing an incremental address counter is sequentially read. An address jump includes detecting an address jump signal, incrementing the
5 incremental address counter, and reading the content of the memory at the incremented address. The content read at the incremented address is transferred into the incremental address counter, and the content of the memory is read at the address contained in the
10 incremental address counter.

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